

## Remarks

### 1. Claim Status

Claims 1-38 remain pending with claims 1, 16, 26, and 34 being independent

### 2. Claim Objection

Applicant has corrected claim 19.

### 3. Prior Art Rejections

#### Claim 1

Claim 1 recites a network interface that initiates DMA (Direct Memory Access) transfers of at least one statistic metering operation of the network interface. As amended, claim 1 further recites that the circuitry initiates the DMA transfer in response to at least one of (1) a configured time interval, and (2) when at least one of the at least one statistic reaches a configured threshold.

Previous to this amendment, the Office Action rejected claim 1 as anticipated by Boucher (U.S. 6,434,620). Boucher does not describe or suggest either initiating transfer based on a configured time interval or initiating a transfer when at least one statistic reaches a configured threshold. Boucher describes a request/response system where statistics are transmitted in response to a UPR (Utility Processor Request) (col. 56, lines 18-33). That is, in response to a given UPR, the INIC will transfer a set of statistics. Boucher does not describe a given UPR as indicating a time interval nor does Boucher describe a UPR as setting up a statistic threshold for triggering a transfer. As

such, Applicant request withdrawal of the rejection of claim 1 as amended and its corresponding dependent claims.

Claim 16

Claim 16, as amended, recites, transferring, by direct memory access, from the network interface to a memory accessed by at least one processor at least one of the statistics metering operation of the network interface in response to at least one statistic reaching a configured threshold. Again as described above, the UPRs of Boucher do not describe or suggest the recited limitations. Accordingly, Applicant requests withdrawal of the rejection of claim 16 as amended and its corresponding dependent claims.

Claim 26

Claim 26, as amended, recites a program product to initiate transfer, by direct memory access, from a network interface to memory accessed by at least one processor at least one of the statistics metering operation of the network interface in response to at least one configured time interval. Again as described above, the UPRs of Boucher do not describe or suggest the recited limitations. Accordingly, Applicant requests withdrawal of the rejection of claim 16 as amended and its corresponding dependent claims.

Claim 36

Claim 36, as amended, recites circuitry, operationally coupled to the direct memory access unit, to initiate direct memory access transfer of multiple ones of the statistics metering operation of the network interface wherein the circuitry to initiate direct memory access transfer initiates direct memory transfer in response to a schedule of times to initiate the transfer. Again, as described above, the UPRs of Boucher do not describe or suggest the recited limitations. Accordingly, Applicant requests withdrawal of the rejection of claim 36 as amended and its corresponding dependent claims.

Respectfully submitted,

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